

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	5	switch with (bipolar adj transistor) with ((wiring adj board) board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 11:31
L4	0	switch with transistor with 257/700,758.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 11:35
L5	89	switch with transistor with ((wiring adj board) PCB (metal adj levels))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 12:03
L6	1	switch same bipolar same amplifier same ((wiring adj board) PCB (metal adj levels))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 12:02
L7	0	(switch same bipolar same ((wiring adj board) PCB (metal adj levels))).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 12:02
L8	8	(switch same transistor same ((wiring adj board) PCB (metal adj levels))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 12:03
L9	1	sato-yusuke.in. and switch and amplifier	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 12:06
L10	1	sato-yusuke.in. and switch and amplifier	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 12:07
S35	0	(switch adj (chip die IC (integrated adj circuit))) same amplifier same (PCB (wiring adj board)) with (emitter adj electrode) with (vdd vss vcc voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/14 21:35

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S36	1	(chip die IC (integrated adj circuit)) same amplifier same (PCB (wiring adj board)) with (emitter adj electrode) with (vdd vss vcc voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 21:35
S37	1	(chip die IC (integrated adj circuit)) with (PCB (wiring adj board)) with (emitter adj electrode) with (vdd vss vcc voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 21:36
S39	117	(chip die IC (integrated adj circuit)) with (emitter adj electrode) with (vdd vss vcc voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 21:47
S40	41	(emitter adj electrode) with (vdd vss vcc voltage) and (PCB (wiring adj board))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 21:42
S41	2	(emitter adj electrode) with (vdd vss vcc voltage) and 257/686.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 21:46
S42	9	emitter with (vdd vss vcc voltage) and 257/686.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 21:59
S43	56	emitter with (vdd vss vcc voltage) same ((wiring adj board) PCB)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:00
S44	72	emitter with (vdd vss vcc voltage ground) same ((wiring adj board) PCB)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:19
S45	16	S44 not S43	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:01
S46	80	emitter with (vdd vss vcc voltage ground grounded grounding GND) same ((wiring adj board) PCB)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:04

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S47	64	S46 not S45	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:04
S48	8	S47 not S44	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:04
S49	12	emitter with (vdd vss vcc voltage ground) and 257/700.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:20
S50	80	emitter with (vdd vss vcc voltage ground grounded grounding) same ((wiring adj board) PCB)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:20
S51	12	emitter with (vdd vss vcc voltage ground\$3) and 257/700.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:20
S53	2	(switch\$3 adj (chip dice die ic circuit)) same amplifier and 257/700.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:23
S54	3	(switch\$3 adj (chip dice die ic circuit)) same amplifier and 257/758.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:24
S55	1	(switch\$3 adj (chip dice die ic circuit)) same amplifier and 257/685-686.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:25
S56	0	(switch\$3 adj (chip dice die ic circuit)) same amplifier and 257/723.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:25
S57	4	(switch\$3 adj (chip dice die ic circuit)) same amplifier and 257/784,786.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:53

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S58	3	bipolar same amplifier and 257/784,786.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 22:57
S59	12	bipolar same amplifier and 257/700,758.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 23:02
S65	8	koyama-kenji.in. and switch and amplifier	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 12:05
S66	1	Miura-toshihiro.in. and switch and amplifier	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/14 23:23
S67	2	kyogoku-toshihiko.in. and switch and amplifier	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/15 11:29